



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

William A. Peterson

Application No.: 10/649,195

Group Art Unit: Unknown

Filing Date: August 27, 2003

Examiner: Not Yet Assigned

For: METHOD AND APPARATUS FOR
PROVIDING A DUAL VOLTAGE
INVERTER

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

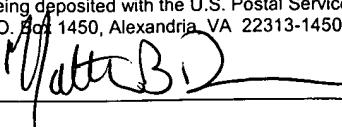
Sir:

Applicant respectfully submits this Information Disclosure Statement and a copy of the cited references pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO-1449 identifies the references of which Applicant is aware.

The Examiner is respectfully requested to consider the references herein and make them of record in the subject application. It is Applicant's position that the references cited pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 are clearly not a bar to allowance of the claims in this application.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: March 19, 2004

Signature: 

Print Name: Matthew B. Dernier

The above-captioned application has not yet received a substantive Office Action, and accordingly it is believed that no fee is necessary. However, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

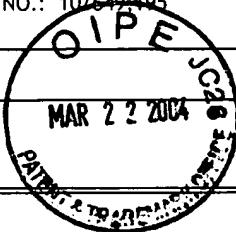
Dated: March 19, 2004

Respectfully submitted,

By 
Matthew B. Dernier
Registration No.: 40,989
KAPLAN & GILMAN, LLP
900 Route 9 North, Suite 104
Woodbridge, New Jersey 07095
(732) 634-7634
Attorneys for Applicant

F:\Clients\E&M Power, Inc\510-5 - Dual Voltage Inverter\IDS - 510-5.doc

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Rev. 2-32) PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO.: 510/5	SERIAL. NO.: 10/649,495
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT: William A. Peterson	
		FILING DATE: August 27, 2003	GROUP:
(Use several sheets if necessary)			

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	6,154,378	11/28/2000	Peterson et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	BA							

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

	CA	Borojevic, Dusan; Nonlinear Algorithms For Fast And Robust Control Of Electrical Drives, Virginia Polytechnic Institute and State University, January 1986, pp. 1-280.					

EXAMINER**DATE CONSIDERED**

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with next
communication to applicant.*